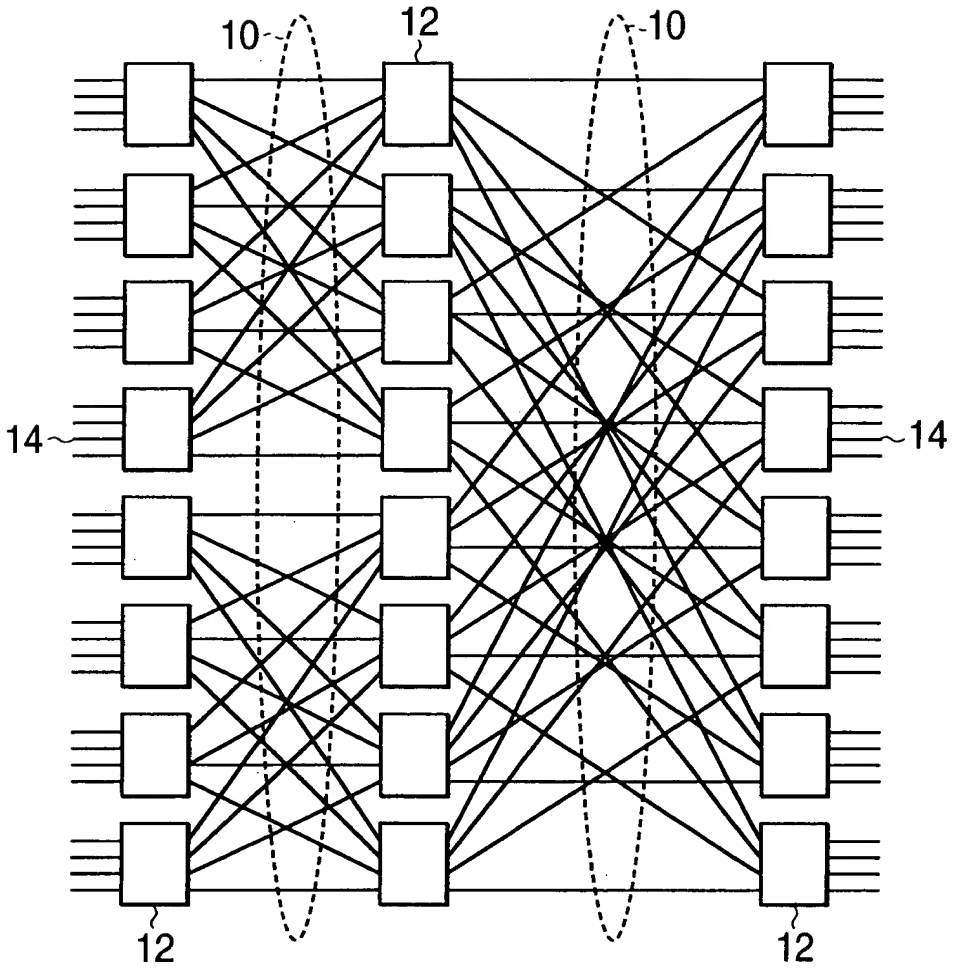
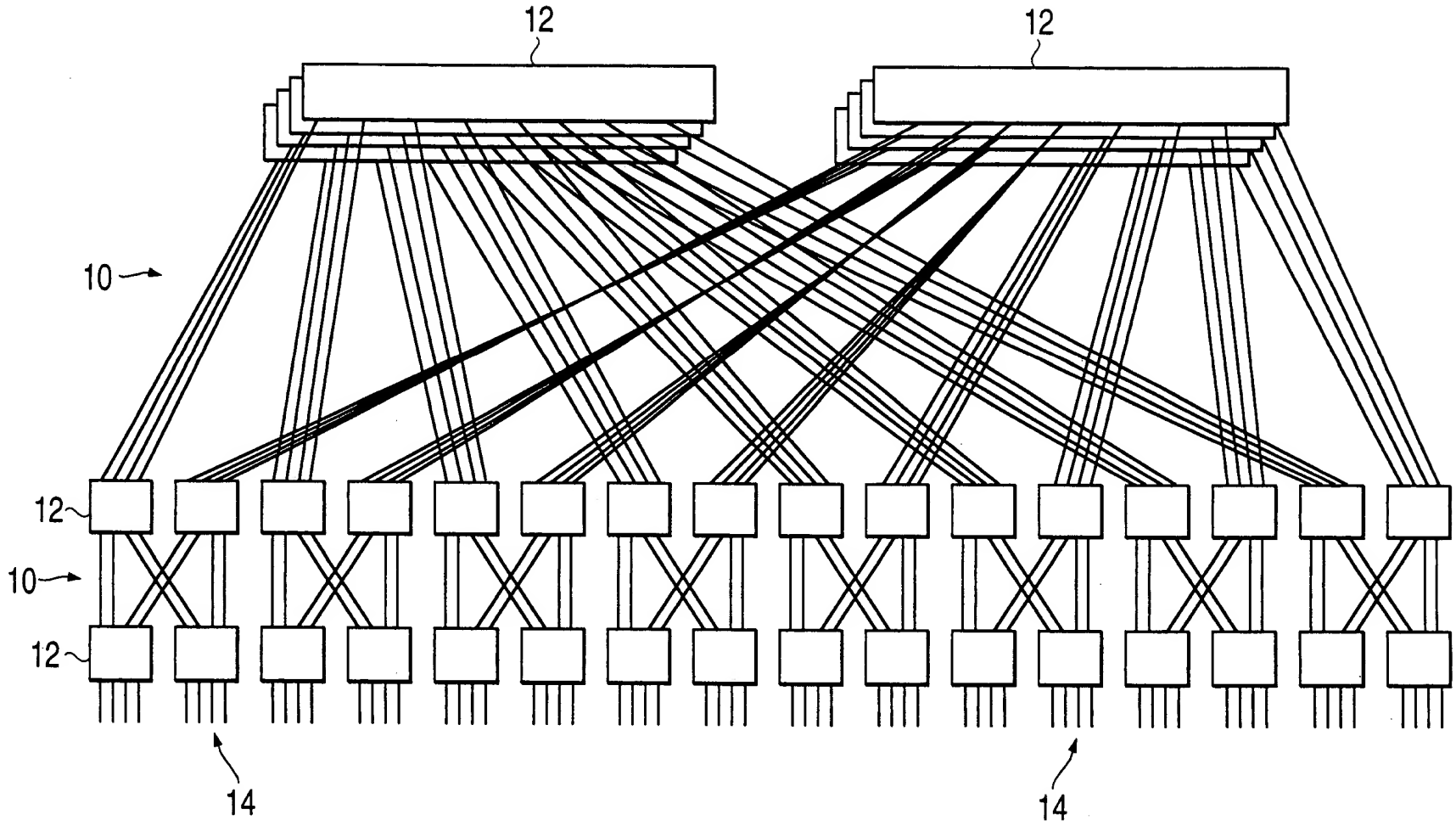
FIG. 1A



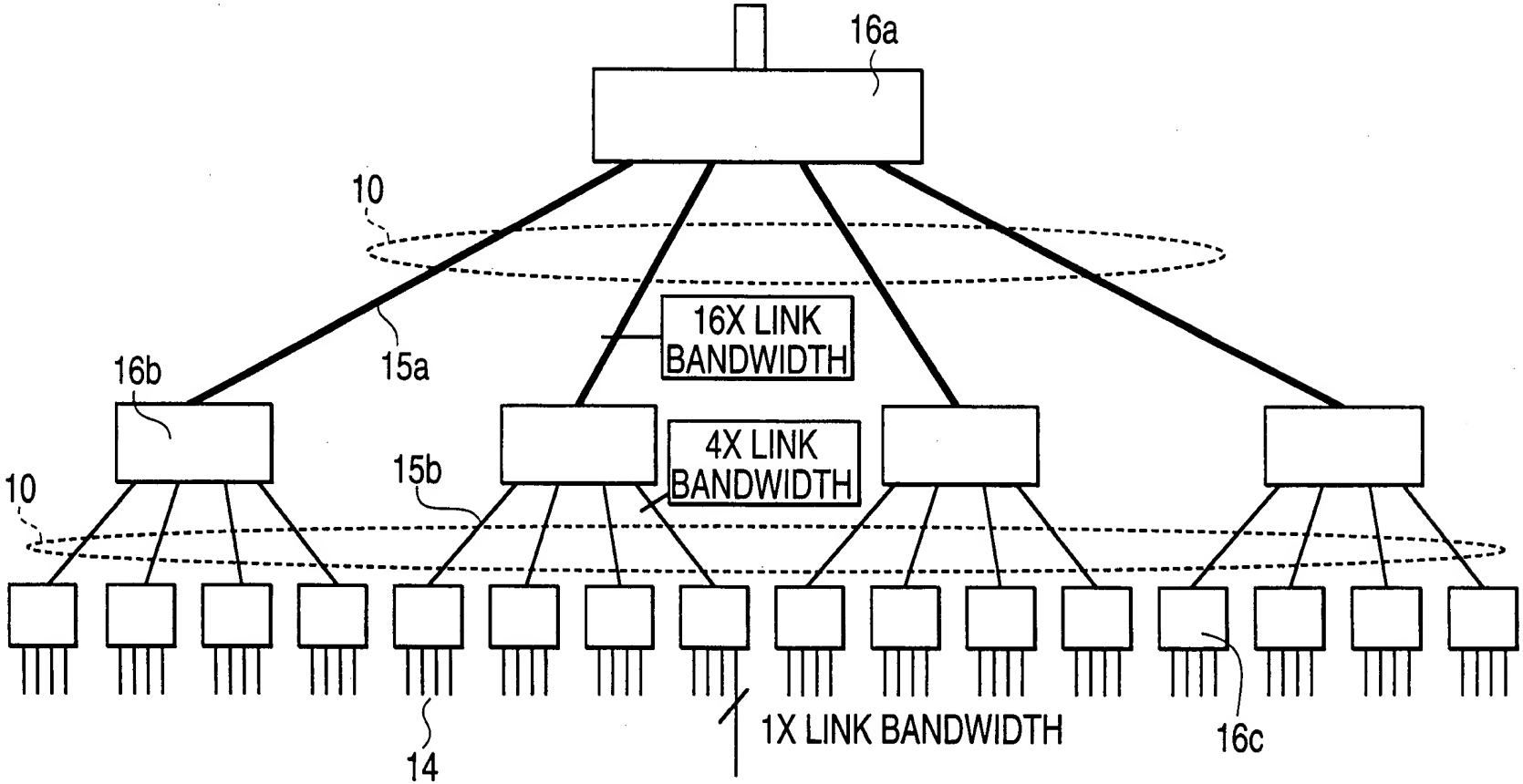
EP 3 050 960 A1

FIG. 1B



EXPRESS MAIL NO. EV 339873360

FIG. 1C



Pages 1 - 2 of 2

FIG. 2

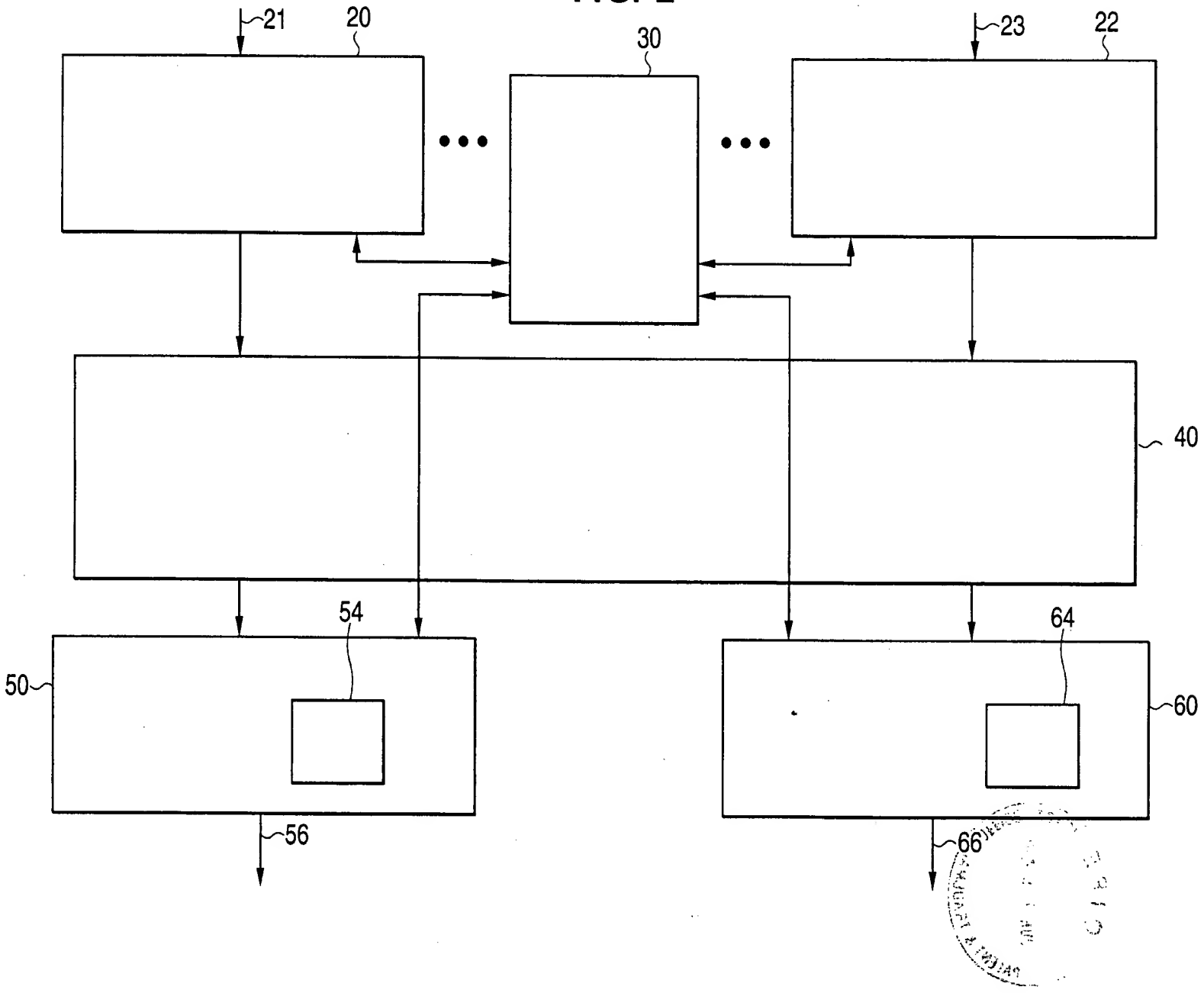


FIG. 3

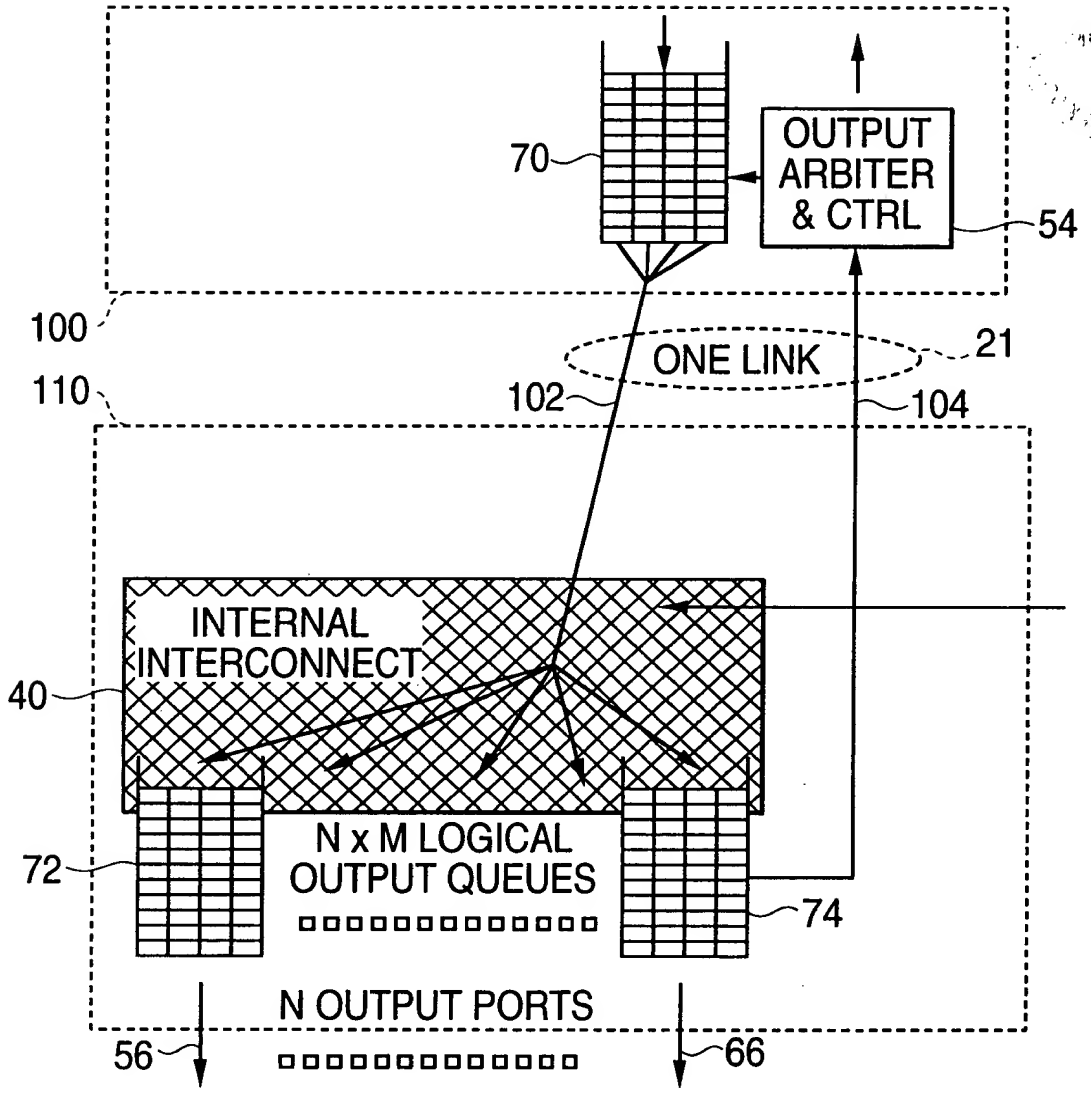

70
OUTPUT ARBITER & CTRL
54
100
ONE LINK
21
110
102
104
INTERNAL INTERCONNECT
40
N x M LOGICAL OUTPUT QUEUES
72
74
N OUTPUT PORTS
56
66

FIG. 4A

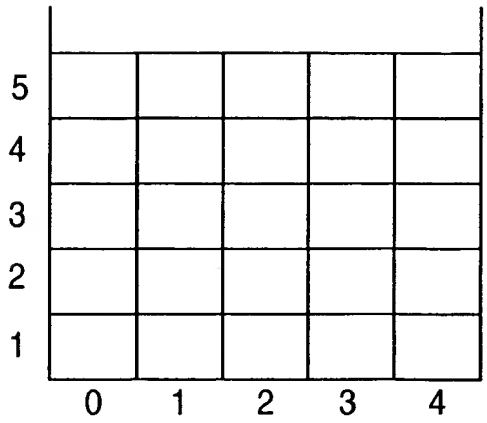


FIG. 4B

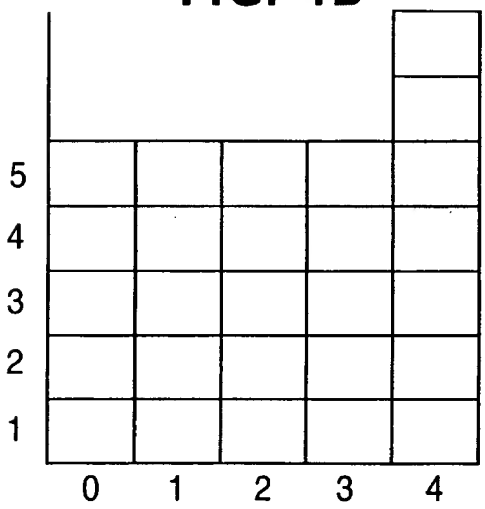


FIG. 4C

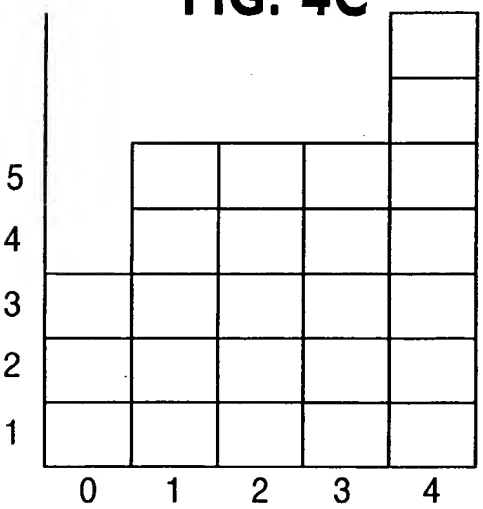


FIG. 5

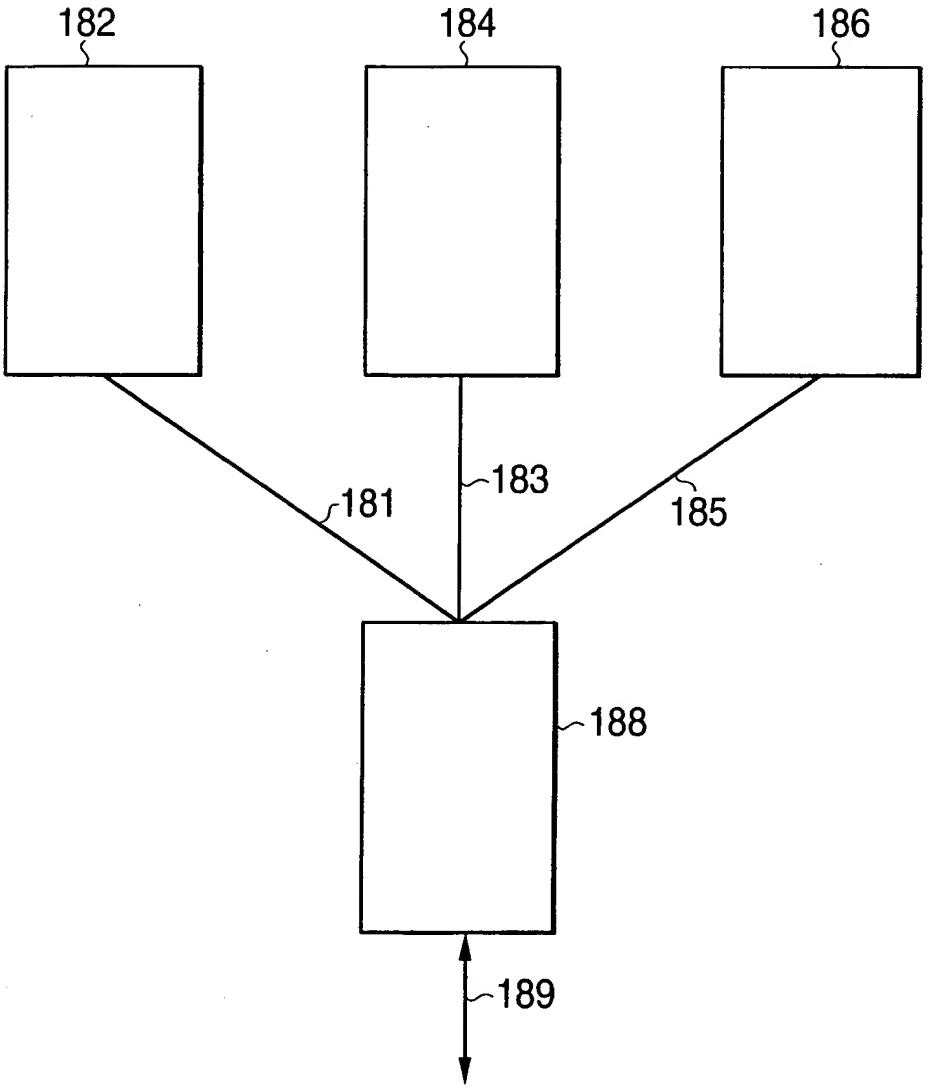
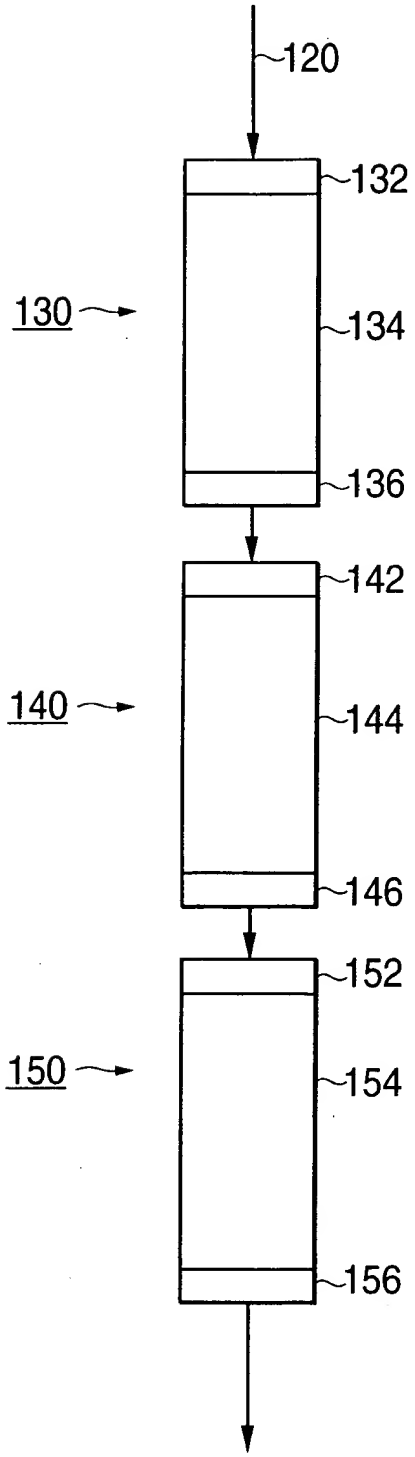
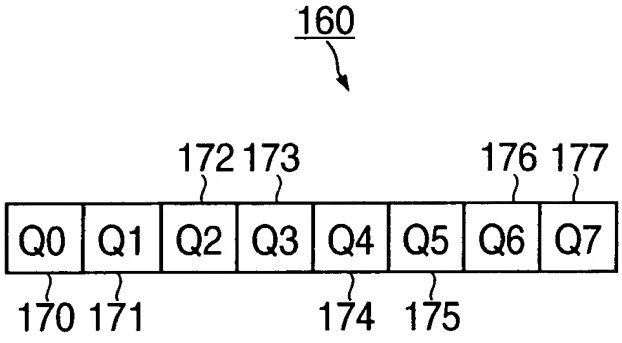


FIG. 6



Docket No. 95-1-0505

FIG. 7



EP 3 090 526 B1

FIG. 8

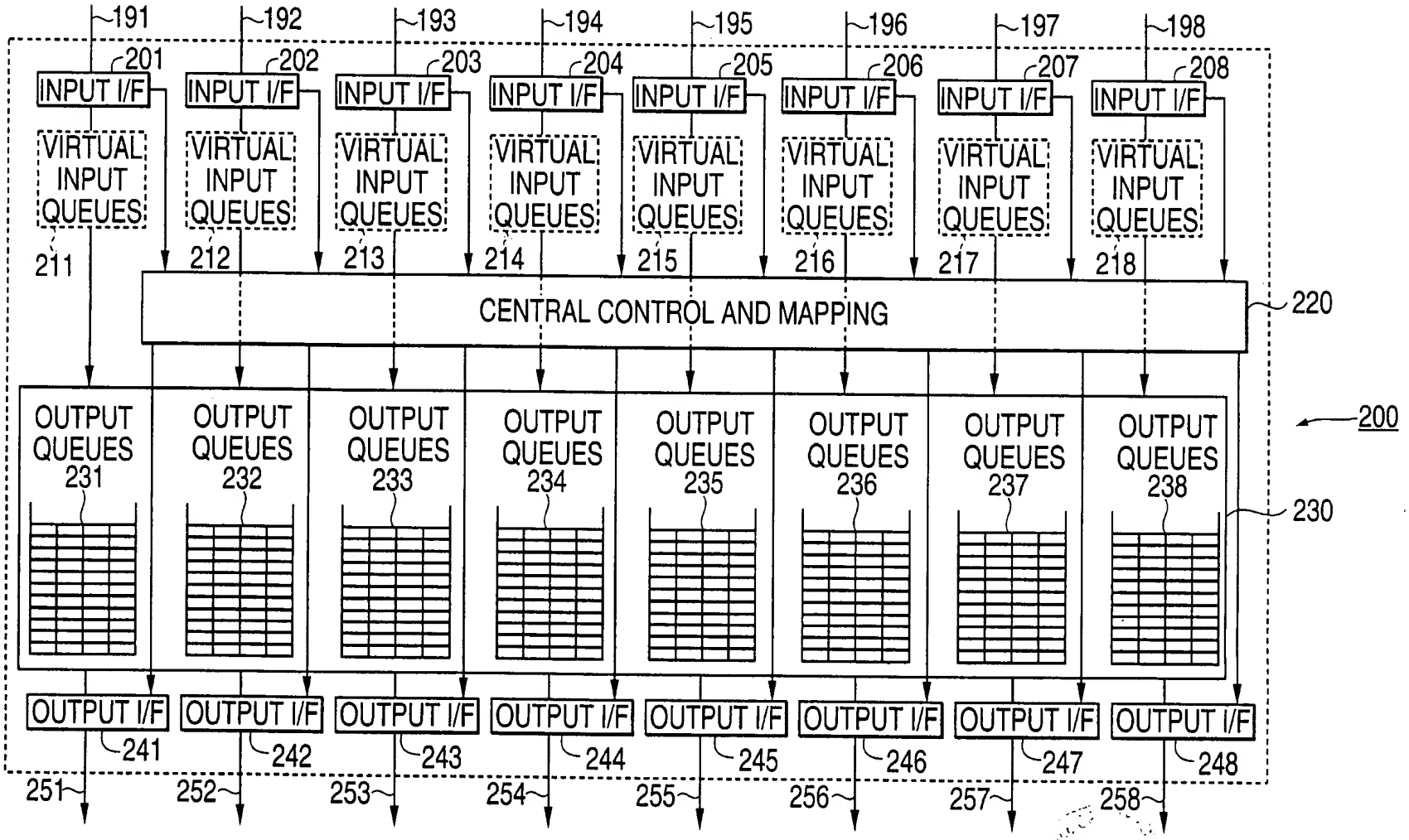


FIG. 9

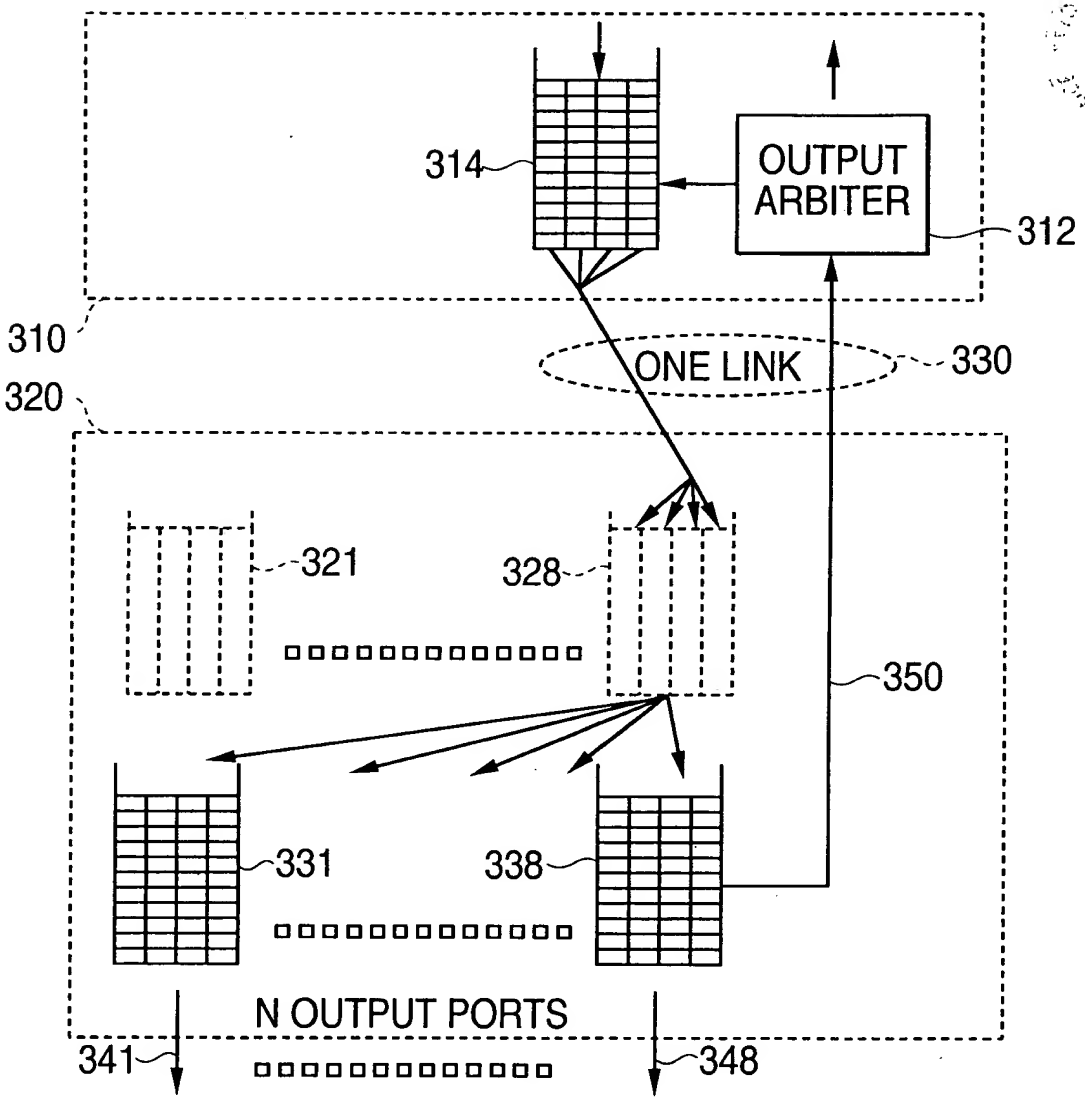
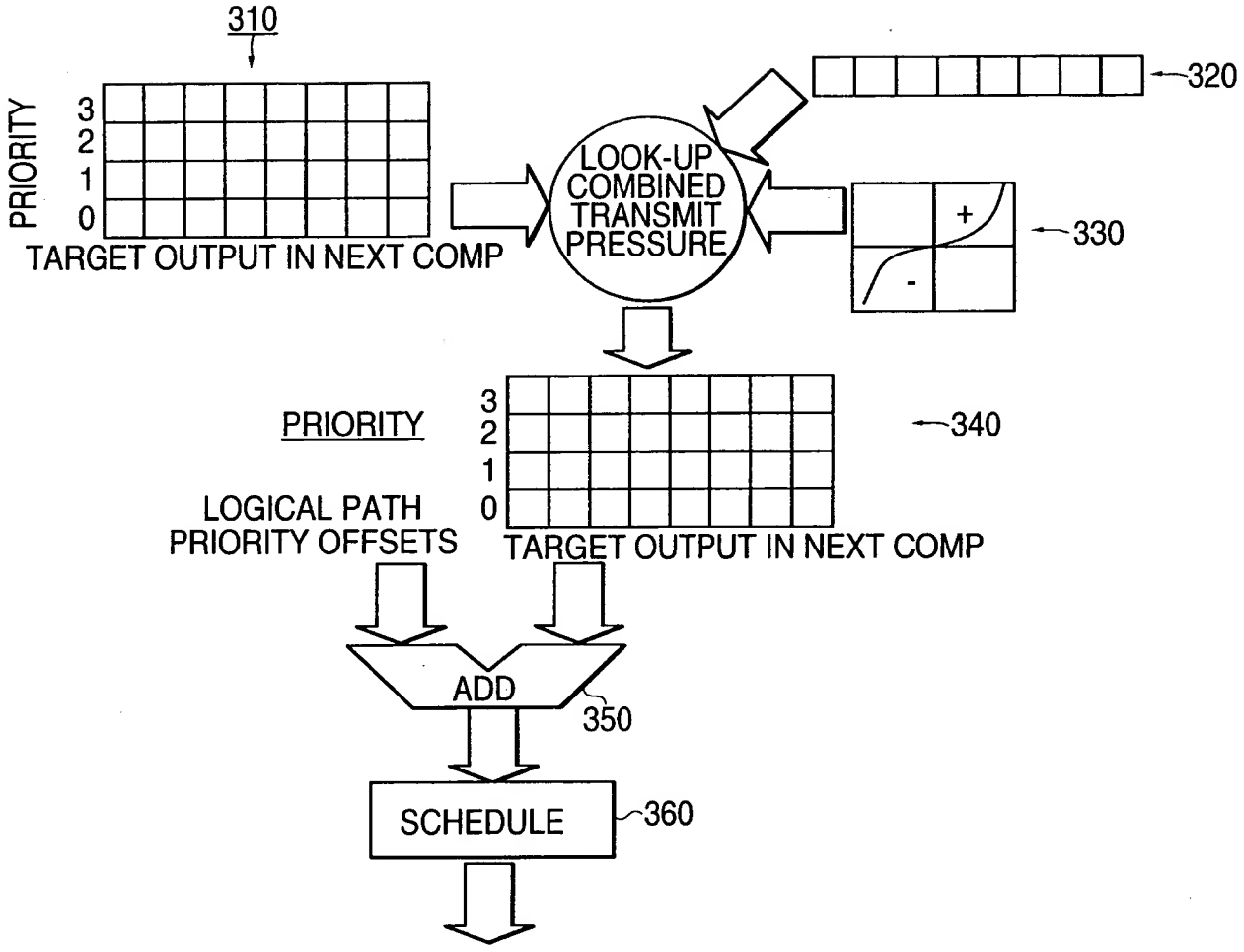
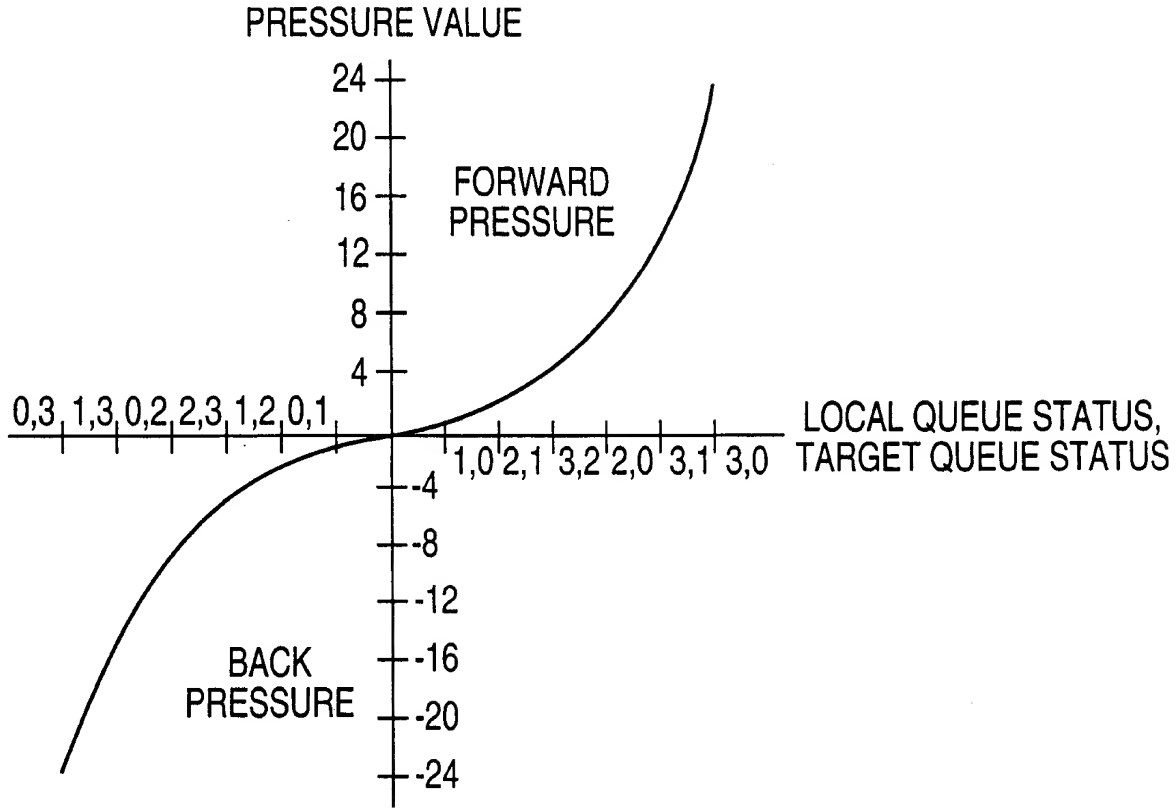

314
OUTPUT ARBITER
312
310
320
ONE LINK
330
321
328
350
331
338
N OUTPUT PORTS
341
348

FIG. 10


310
PRIORITY
3
2
1
0
TARGET OUTPUT IN NEXT COMP
320
LOOK-UP
COMBINED
TRANSMIT
PRESSURE
+
-
330
PRIORITY
3
2
1
0
340
LOGICAL PATH
PRIORITY OFFSETS
TARGET OUTPUT IN NEXT COMP
ADD
350
SCHEDULE
360

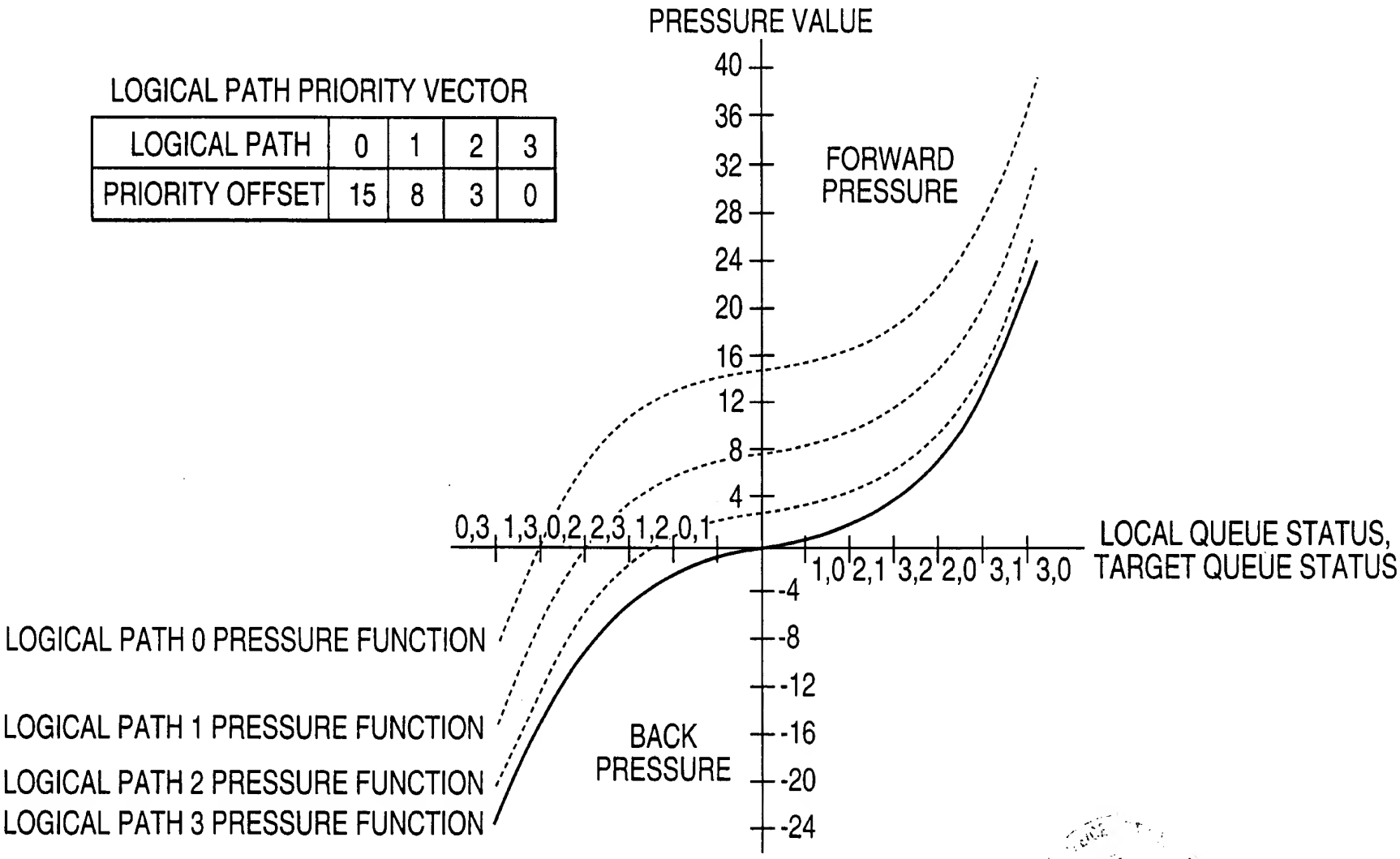
FUJITSU 5,390,360

FIG. 11



		STATUS OF TARGET QUEUE IN NEXT COMP			
		0	1	2	3
STATUS	0	0	-1	-8	-24
OF	1	1	0	-2	-14
LOCAL	2	8	2	0	-4
QUEUE	3	24	14	4	0

FIG. 12

LOGICAL PATH PRIORITY VECTOR

LOGICAL PATH	0	1	2	3
PRIORITY OFFSET	15	8	3	0